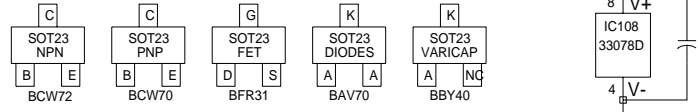


PINOUTS AS VIEWED FROM ABOVE



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T750
UHF DUPLEX BOARD

IPN: 220-01189-05 ISSUE: B 2.SC. 1

PROJECT: T750 DESIGNER: M.HALL FILE NAME: 118905a FILE DATE: 9-Sep-99 NO.SHEETS: 1

| | | | | | | | |
|---------|------------|-------------------|--------|--------|--------|-------|----------|
| 5B | ECO 740090 | USES UPDATED TCXO | ADAM.D | M.HALL | M.HALL | HAN.O | 09/09/99 |
| 5A | ECO 770159 | | M.HALL | | | | |
| REV/ISS | AMENDMENTS | | DRAWN | CHKD | D.O. | APVD | DATE |

6

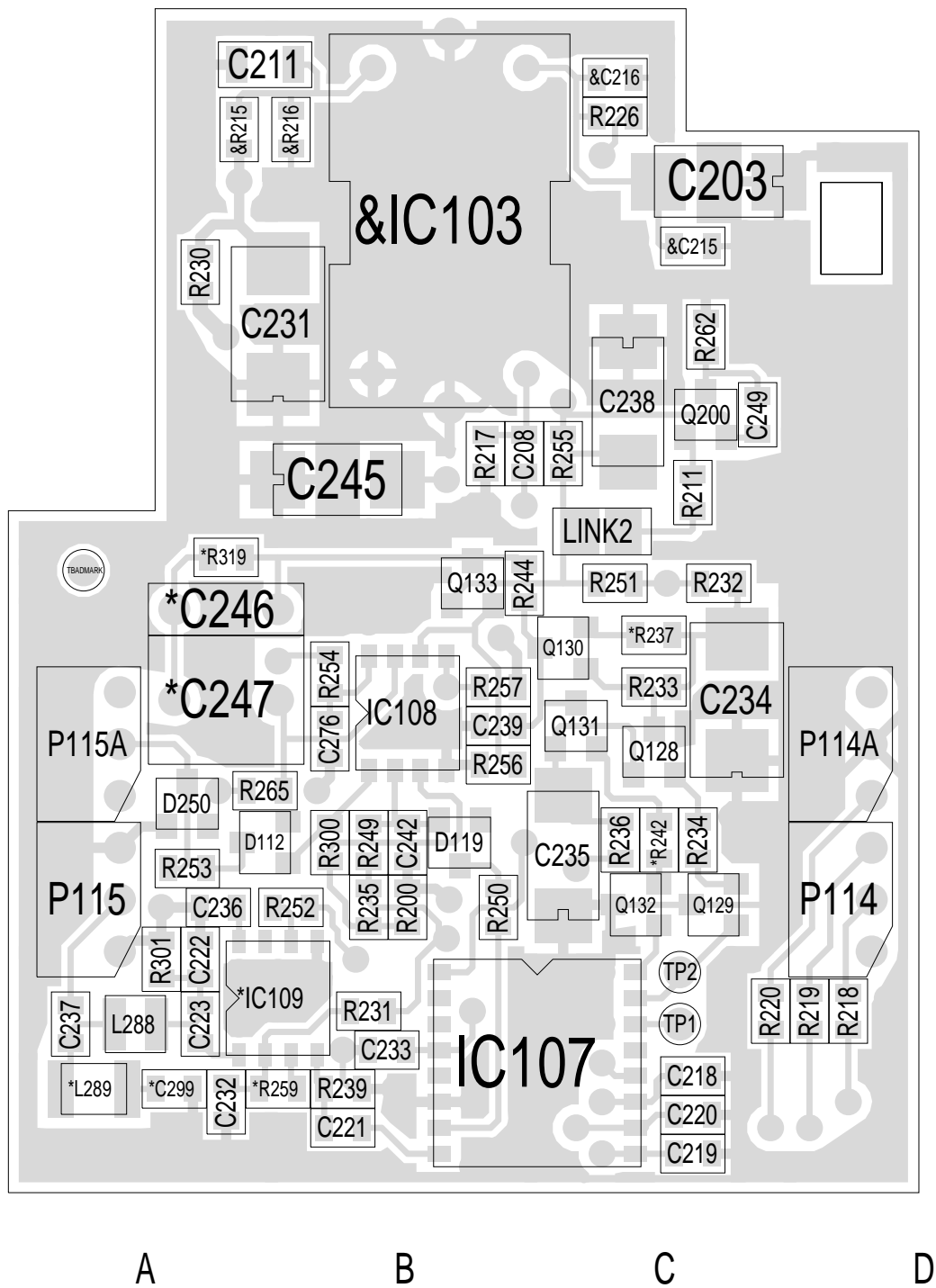
5

4

3

2

1



TAIT ELECTRONICS IPN: 220-01189-05 ISS: A ID: 1.TA DATE: 5 May 1999
T750 DUPLEX PCB LAYOUT - TOP SIDE

Scale:3:1 ; Rotation: 0 degrees

6

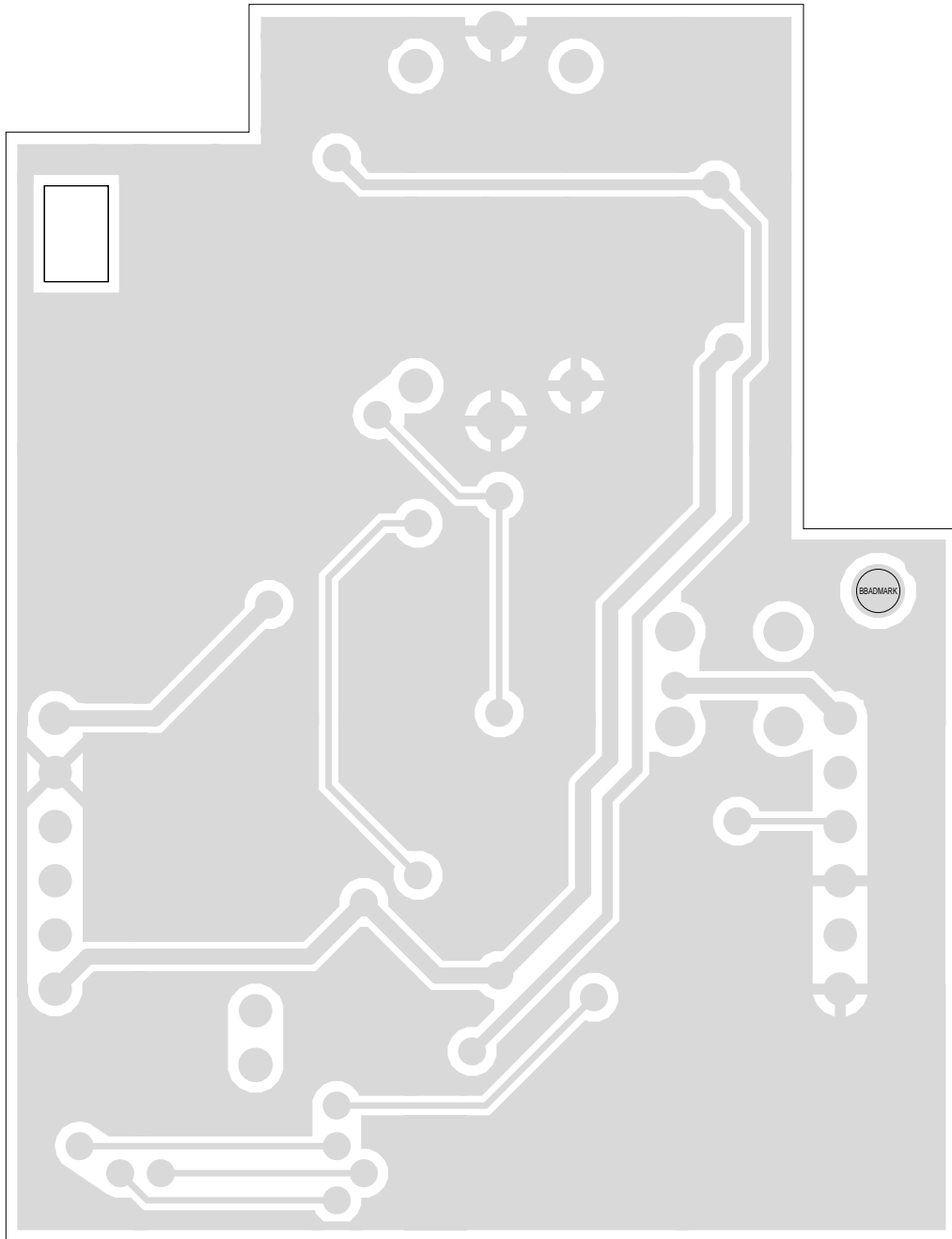
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TAIT ELECTRONICS

IPN: 220-01189-05

ISS: A

ID: 2.BA

DATE: 5 May 1999

T750 DUPLEX PCB LAYOUT - BOTTOM SIDE

Scale:3:1 ; Rotation: 0 degrees